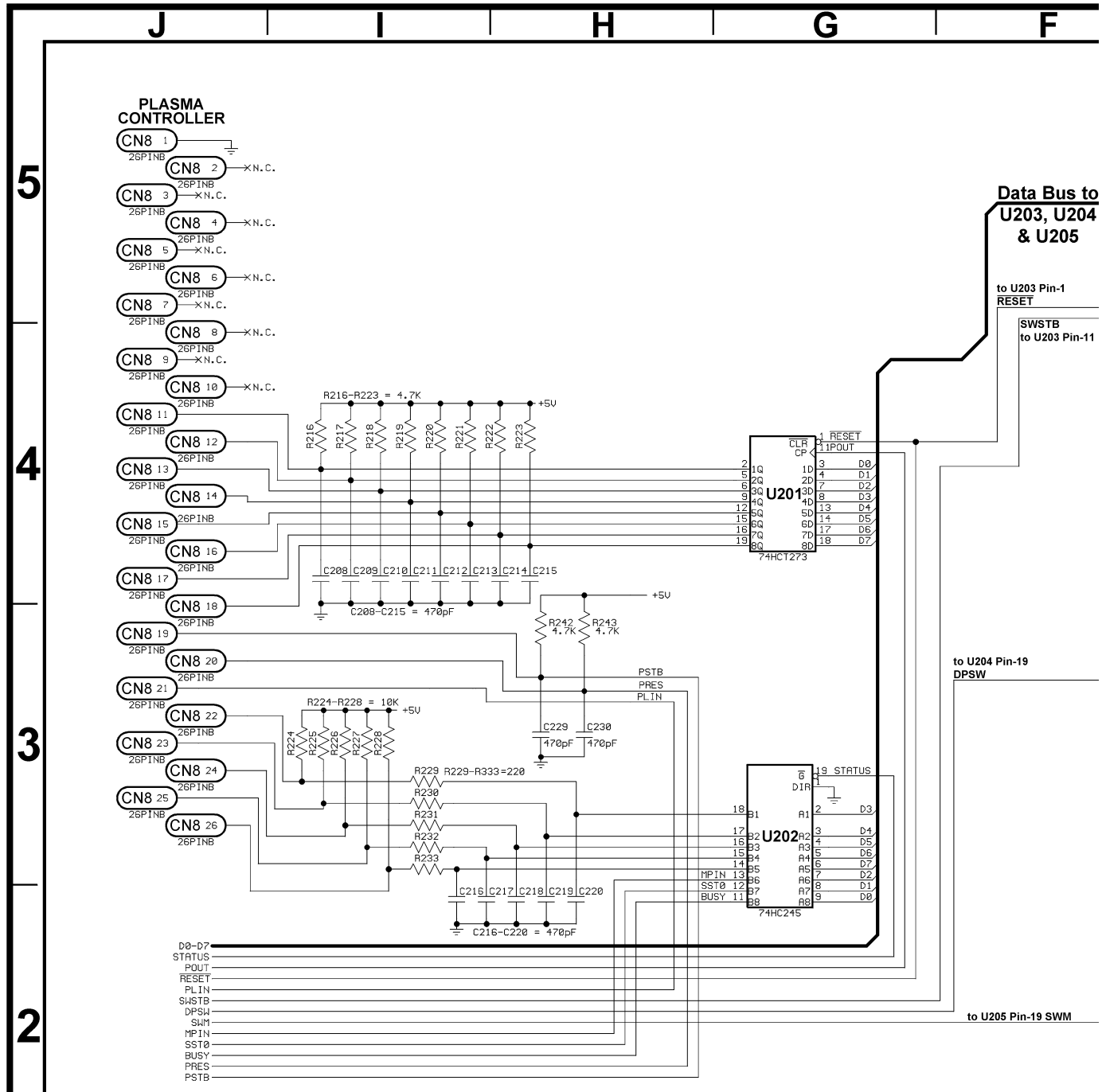


Excerpt from CPU/Sound Board Theory of Operation (re: Plasma Interface):

Plasma Interface: The data path for communication to and from the Plasma Controller Board is 8 bits wide. There are separate *Input* and *Output Busses*. The *Input Bus* from the Plasma Controller to the CPU/Sound Board comes in on **CN8 [PLASMA CONTROL]-Pins 3-10** and is fed into **U200** for input to the CPU's *Data Bus*. Data going out to the controller comes from the CPU's *Data Bus* through **U201** and onto **CN8-Pins 11-18**. Status back from the Plasma Controller comes in on **CN8-Pins 22-26** and is fed into **U202** for input to the CPU's *Data Bus*. Two control signals that go out to the Plasma Controller are **PRES [PLASMA RESET]** and **CN8-Pin 19 [PSTB - Plasma Strobe]**. The Plasma Reset is software controllable through **U216/B** and also has a test point "Plasma Reset". The *Plasma Strobe Signal* to the controller is generated from **U216/A** and is used to latch data into the Plasma Controller.

CPU/Sound Board Schematic (Sheet 3 of 3)



Please phone or eMail with any questions or comments at the below numbers or address.

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